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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/586,218	07/14/2006	Frik J. Marinissen	NL04 0065 US1	2749		
65913	7590 02/13/200		NEAT OUL CST	2,40		
NXP, B.V.	7590 02/13/200	79	EXAM	EXAMINER		
NXP INTELLECTUAL PROPERTY DEPARTMENT			KERVERO	KERVEROS, JAMES C		
M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131			ART UNIT	PAPER NUMBER		
		2117				
			NOTIFICATION DATE	DELIVERY MODE		
			02/13/2009	EL ECTRONIC		

## Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

## **Advisory Action** Before the Filing of an Appeal Brief

Ī	Application No.	Applicant(s)		
	10/586,218	MARINISSEN ET AL.		
	Examiner	Art Unit		
	JAMES C. KERVEROS	2117		

JAMES	S C. KERVEROS	2117	
The MAILING DATE of this communication appears on	the cover sheet with the c	orrespondence addi	ress
THE REPLY FILED 05 February 2009 FAILS TO PLACE THIS APPLIC	CATION IN CONDITION FO	R ALLOWANCE.	
<ol> <li>M The reply was filed after a final rejection, but prior to or on the san application, applicant must timely file one of the following replies: application in condition for allowance; (2) a Notice of Appeal (with for Continued Examination (RCE) in compliance with 37 CFR 1.11 periods:</li> </ol>	ne day as filing a Notice of A (1) an amendment, affidavit appeal fee) in compliance v	Appeal. To avoid aban i, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expiresmonths from the mailing date of	the final rejection.		
b) The period for reply expires on: (1) the mailing date of this Advisory A no event, however, will the statutory period for reply expire later than Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY	SIX MONTHS from the mailing	date of the final rejectio	n.
MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).  Extensions of time may be obtained under 37 CFR 1.136(a). The date on which	the polition under 27 CER 1 13	26(a) and the appropriate	ovtoncion foo
Latelistors of life life ye document units of york's 1,000/2, "He date of what have been filled it the date for purposes of determining the period of extension a under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened set forth in (b) above, if checked. Any reply received by the Office later than thre may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL.	nd the corresponding amount of distatutory period for reply origin	of the fee. The appropria nally set in the final Office	ite extension fee action; or (2) as
<ol> <li>The Notice of Appeal was filed on A brief in compliance w filing the Notice of Appeal (37 CFR 41.37(a)), or any extension the Notice of Appeal has been filed, any reply must be filed within the</li> </ol>	ereof (37 CFR 41.37(e)), to	avoid dismissal of the	
AMENDMENTS	time period sectoral in 57 c	51 TC 4 1.57 (a).	
The proposed amendment(s) filed after a final rejection, but prior  (a) They raise new issues that would require further considerati  (b) They raise the issue of new matter (see NOTE below);			cause
(c) They are not deemed to place the application in better form appeal; and/or	for appeal by materially red	lucing or simplifying th	ne issues for
(d) ☐ They present additional claims without canceling a correspondance (See 37 CFR 1.116 and 41.33(a)).	onding number of finally reje	cted claims.	
4. The amendments are not in compliance with 37 CFR 1.121. See	attached Notice of Non-Cor	mpliant Amendment (F	PTOL-324).
5. Applicant's reply has overcome the following rejection(s):		ν.	
Newly proposed or amended claim(s) would be allowable non-allowable claim(s).		•	
7. For purposes of appeal, the proposed amendment(s): a) will report how the new or amended claims would be rejected is provided be The status of the claim(s) is (or will be) as follows:		be entered and an ex	planation of
Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: 1-25.			
Claim(s) rejected: 1-25. Claim(s) withdrawn from consideration:			
AFFIDAVIT OR OTHER EVIDENCE			
<ol> <li>The affidavit or other evidence filed after a final action, but before because applicant failed to provide a showing of good and sufficie was not earlier presented. See 37 CFR 1.116(e).</li> </ol>			
<ol> <li>The affidavit or other evidence filed after the date of filing a Notice entered because the affidavit or other evidence failed to overcome showing a good and sufficient reasons why it is necessary and wa</li> </ol>	e <u>all</u> rejections under appea	l and/or appellant fails	to provide a
10. The affidavit or other evidence is entered. An explanation of the REQUEST FOR RECONSIDERATION/OTHER	status of the claims after en	try is below or attache	ed.
11.   The request for reconsideration has been consideration has been consideration has been consideration.  The request for reconsideration has been considered him the reconsideration has been conside	dered but does NOT place t	he application in cond	lition for
See Continuation Sheet.	2/00) D N-/-)		
<ul> <li>12. Note the attached Information Disclosure Statement(s). (PTO/SE</li> <li>13. Other:</li> </ul>	o/oo; raper No(s)		
	/JAMES C KERVEROS/	1	

Primary Examiner, Art Unit 2117

Continuation of 11, does NOT place the application in condition for allowance because: The amendment after final rejection filed on 2/5/2009 has been entered.

Applicant's arguments filed on 2/5/2009, with respect to the rejection of Claims 1-25 under 35 U.S.C. 102(b) as being anticipated by Bhattacharya (US Patent No. 6,378,090), have been fully considered but they are not persuasive, for the reasons as set forth in the FINAL Office Action mailed 1/2/10/2008

In response to Applicant's argument that Bhattacharya does not teach that the disable TMS signal (i. e., the asserted global enable signal) is provided to each of the embedded cores 720 and 730 in a test mode, clearly Bhattacharya discloses the "Disable TMS signal is supplied to AND gates 741 and 743 (Fig. 7) to control supply of test data input TDI to the corresponding embedded cores 720 and 730. This Disable TMS signal is high-inactive when snoopy test access controller 921 is in a snoopy state. This permits the supply of the test mode select signal to the embedded cores' (Col. 11, lines 30-67). In office words, when the Disable TMS signal is high, it enables the test mode select signals TMS2 and TMS3 through the respective AND gates 741 and 743 to the corresponding embedded cores, thus placing the cores 720 and 730 in a test mode.

Date: 10 February 2009 Office Action: ADVISORY

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